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	(Depositor's name)
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May 2, 2007	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/682,323	10/09/2003	Joseph A. Yedinak	90065.000431/17732.56390.	9818

TITLE OF INVENTION: VOLTAGE DIVIDER FIELD PLATE TERMINATION WITH UNEQUAL FIXED BIASING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	$1400	$300	$0	$1700	05/02/2007

EXAMINER	ART UNIT	CLASS-SUBCLASS
DICKEY, THOMAS L	2826	257-500000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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1 Thomas R. FitzGerald, Esq
2 Hiscock & Barclay, LLP
3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE: FAIRCHILD SEMICONDUCTOR CORPORATION

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Please check the appropriate assignee category or categories (will not be printed on the patent) : ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
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